

E 06 ELECTRICAL & ELECTRONIC
COLLEGE : GOA COLLEGE OF ENGINEERING

** RC2007-08 **
EXAM YEAR : MAY-2015

SEAT NO.	P.R. NO.	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY MAX. 100 MIN. 040	SESSIONAL 25	TOTAL 125 50	PRACTICAL 50 OR 25 20 OR 10	ORAL 50 OR 25 20 OR 10	REMARKS MAX.850 MIN.340
PAPER DESCRIPTION											
0029 200807800 (E) N M ALEX LONGJAM						** RESULT WITHHELD - (SEM. 6 NOT PASSED) **					
					VLSI CIRCUIT DESIGN	035	007	042F		025E	
					ELECTRICAL POWER SYSTEM II	049	004	053+			
					ADVANCED CONTROLLED DRIVES	056	000	056E			
					NEURAL NETWORKS & FUZZY LOGIC	040	004	044E	#6	035E	
					FLEXIBLE AC TRANSMISSION SYSTEM	048	000	048E	#2	035E	
					PROJECT		015+			034+	
0030 200907816 (E) N M CHARI BAGAR DATTA											
					VLSI CIRCUIT DESIGN	042	010	052+		036+	
					ELECTRICAL POWER SYSTEM II	054	010	064+			
					ADVANCED CONTROLLED DRIVES	050	010	060+			
					NEURAL NETWORKS & FUZZY LOGIC	041	001	042F		025+	
					FLEXIBLE AC TRANSMISSION SYSTEM	046	008	054+		029+	
					PROJECT		020+			045+	
											427 F
0031 201105487 (E) N M CHAUBEY MANISH KUMAR						** RESULT WITHHELD - (SEM. 6 NOT PASSED) **					
					VLSI CIRCUIT DESIGN	035	#5	010	045+	#5	030+
					ELECTRICAL POWER SYSTEM II	055		013	068+		
					ADVANCED CONTROLLED DRIVES	049		005	054+		
					NEURAL NETWORKS & FUZZY LOGIC	055		010	065P		030+
					FLEXIBLE AC TRANSMISSION SYSTEM	039	#1	010	049+	#1	034+
					PROJECT			019+		045+	
0032 201105490 (E) N M DEV KAPIL HEMANT											
					VLSI CIRCUIT DESIGN	040	014	054+		033+	
					ELECTRICAL POWER SYSTEM II	041	010	051+			
					ADVANCED CONTROLLED DRIVES	053	002	055+			
					NEURAL NETWORKS & FUZZY LOGIC	026	010	036F		034+	
					FLEXIBLE AC TRANSMISSION SYSTEM	040	010	050+		036+	
					PROJECT			023+		043+	
											415 F
0033 201105825 (E) N M HIJAM MARTIN LUWANG											
					VLSI CIRCUIT DESIGN	049	005	054+		037P	
					ELECTRICAL POWER SYSTEM II	052	002	054+			
					ADVANCED CONTROLLED DRIVES	067	000	067+			
					NEURAL NETWORKS & FUZZY LOGIC	063	000	063+		038P	
					FLEXIBLE AC TRANSMISSION SYSTEM	064	000	064P		038P	
					PROJECT			016+		033+	
											464 P

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NO.	CD.	VI.	PAPER DESCRIPTION						
0034	201008360	(E)	N M LOPES FRANCIS BOMDIN						** RESULT WITHHELD - (SEM. 6 NOT PASSED) **
			VLSI CIRCUIT DESIGN	027	006	033F		035+	
			ELECTRICAL POWER SYSTEM II	052	016	068+			
			ADVANCED CONTROLLED DRIVES	067	010	077+			
			NEURAL NETWORKS & FUZZY LOGIC	045	015	060+		022+	
			FLEXIBLE AC TRANSMISSION SYSTEM	056	014	070+		037+	
			PROJECT		020+			044+	
0035	201004597	(E)	N M MALANJA KHURAIJAM						
			VLSI CIRCUIT DESIGN	017	010	027F		025E	
			ELECTRICAL POWER SYSTEM II	042	009	051+			
			ADVANCED CONTROLLED DRIVES	042	003	045E	\$5		
			NEURAL NETWORKS & FUZZY LOGIC	041	000	041F		040E	
			FLEXIBLE AC TRANSMISSION SYSTEM	052	000	052+		040E	
			PROJECT		016+			032+	369 \$05 F
0036	201105306	(E)	N M PHADTE NARAYAN SANJAY						
			VLSI CIRCUIT DESIGN	064	019	083P		043+	
			ELECTRICAL POWER SYSTEM II	051	016	067+			
			ADVANCED CONTROLLED DRIVES	056	006	062+			
			NEURAL NETWORKS & FUZZY LOGIC	043	012	055+		038+	
			FLEXIBLE AC TRANSMISSION SYSTEM	060	010	070+		036+	
			PROJECT		021+			040+	515 P
0037	201104695	(E)	N M SARVESH NARSINHA PRABHU						** RESULT WITHHELD - (SEM. 6 NOT PASSED) **
			VLSI CIRCUIT DESIGN	035	\$5 010	045+	\$5	027+	
			ELECTRICAL POWER SYSTEM II	042	017	059+			
			ADVANCED CONTROLLED DRIVES	070	004	074E			
			NEURAL NETWORKS & FUZZY LOGIC	040	010	050E		030+	
			FLEXIBLE AC TRANSMISSION SYSTEM	031	010	041F		037+	
			PROJECT		015+			025+	
0038	201105582	(E)	N M VARGHESE SANCHU						** RESULT WITHHELD - (SEM. 6 NOT PASSED) **
			VLSI CIRCUIT DESIGN	063	017	080+		038+	
			ELECTRICAL POWER SYSTEM II	056	016	072+			
			ADVANCED CONTROLLED DRIVES	057	003	060P			
			NEURAL NETWORKS & FUZZY LOGIC	060	014	074+		038+	
			FLEXIBLE AC TRANSMISSION SYSTEM	054	010	064+		038+	
			PROJECT		022+			042+	

READ BY *[Signature]*

CHECKED BY *[Signature]*

DECLARED ON 29/7/2015

ASST. REGISTRAR-E (PROF) *[Signature]*

CONTROLLER OF EXAMINATIONS *[Signature]* 29/7/15

REGISTRAR *[Signature]*